DATE 2017 Awards Ceremony

2017-03-28

**PhD Forum Best Poster Prize supported by EDAA, ACM Sigda and IEEE CEDA**

Enabling Caches in Probabilistic Timing Analysis
Leonidas Kosmidis, Barcelona Supercomputing Center, ES

Towards Computer-Aided Design of Quantum Logic
Philipp Niemann, University of Bremen, DE

**IEEE Fellow Award**

Prof. Todd Austin
University of Michigan
elevated to IEEE Fellow “for contributions to simulation techniques and resilient system design in computer architecture”

Prof. Valeria Bertacco
University of Michigan
elevated to IEEE Fellow “for contribution to computer-aided verification and reliable system design”

Prof. Cristina Silvano
Politecnico di Milano
elevated to IEEE Fellow “for contributions to energy-efficient computer architectures”

Prof. Alex Yakovlev
Newcastle University
elevated to IEEE Fellow “for contributions to theory and design of asynchronous circuits”
DATE Fellow Award

Dr. Juergen Haase
edacentrum
For long term outstanding contribution to the DATE Exhibition Theatre

Prof. Luca Fanucci
Università di Pisa
For outstanding service contribution as General Chair of DATE 2016

IEEE CEDA Service Award

Prof. Luca Fanucci
For outstanding service contribution as General Chair of DATE 2016
IEEE CS TTTC Outstanding Contribution Award

Prof. Luca Fanucci
For outstanding service contribution as General Chair of DATE 2016

IEEE TTTC Lifetime Contribution Medal

Bernard Courtois
For outstanding technical contribution and fundamental impact on Test Technology

DATE 2017 Best Paper Awards

Each year the Design, Automation and Test in Europe Conference presents awards to the authors of the best papers. The selection is performed by an award committee, based on the results of the reviewing process and the quality of the final paper.

DATE Best Paper Awards have been given during the DATE Party

D Track

AUTOMATIC PLACE-AND-ROUTE OF EMERGING LED-DRIVEN WIRES WITHIN A MONOLITHICALLY-INTEGRATED CMOS+III-V PROCESS

Tushar Krishna¹, Arya Balachandran², Siau Ben Chiah², Li Zhang³, Bing Wang³, Cong Wang², Kenneth Lee Eng Kian³, Jurgen Michel⁴ and Li-Shiuan Peh⁵
A Track

COSYN: EFFICIENT SINGLE-CELL ANALYSIS USING A HYBRID MICROFLUIDIC PLATFORM
Mohamed Ibrahim¹, Krishnendu Chakrabarty¹ and Ulf Schlichtmann²
¹Duke University, US; ²TU München, DE

T Track

FAST AND WAVEFORM-ACCURATE HAZARD-AWARE SAT-BASED TSOF ATPG
Jan Burchard¹, Dominik Erb¹, Adit D. Singh³, Sudhakar M. Reddy³ and Bernd Becker¹
¹University of Freiburg, DE; ²Auburn University, US; ³University of Iowa, US

E Track

MODNN: LOCAL DISTRIBUTED MOBILE COMPUTING SYSTEM FOR DEEP NEURAL NETWORK
Jiachen Mao¹, Xiang Chen², Kent W. Nixon¹, Christopher Krieger³ and Yiran Chen¹
¹University of Pittsburgh, US; ²George Mason University, US; ³University of Maryland, Baltimore County, US

DATE 2017 Best IP Award
DATE Best IP Award has been given during the DATE Party

EVOAPPROX8B: LIBRARY OF APPROXIMATE ADDERS AND MULTIPLIERS FOR CIRCUIT DESIGN AND BENCHMARKING OF APPROXIMATION METHODS (Paper/SoftConf ID: 241)
Vojtech Mrazek, Radek Hrbacek, Zdenek Vasicek and Lukas Sekanina, Brno University of Technology, CZ

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